

## Introduction

This document contains information necessary to successfully install your hardware.

**WARNING** The STP-3110 is not intended for use in circuits carrying voltages in excess of those for which the inputs of Models KPCI-3110 and KPCI-3116 are rated, typically  $\pm 10\text{V}$ . Refer to the KPCI-3110 and KPCI-3116 User's Manual for these products for additional limits.

## Installation

As described in the International Electrotechnical Commission (IEC) Standard IEC 664, the signal terminals are Installation Category I and must not be connected to mains.

Refer to "Attaching the STP-3110" in the KPCI-3110 and KPCI-3116 User's Manual.

# Screw terminal assignments for J1

TB #	J1 Pin #	Description	TB #	J1 Pin #	Description
1	25	Analog In 0	2	50	Analog In 8/0 Return
3	24	Analog In 1	4	49	Analog In 9/1 Return
5	23	Analog In 2	6	48	Analog In 10/2 Return
7	22	Analog In 3	8	47	Analog In 11/3 Return
9	21	Analog In 4	10	46	Analog In 12/4 Return
11	20	Analog In 5	12	45	Analog In 13/5 Return
13	19	Analog In 6	14	44	Analog In 14/6 Return
15	18	Analog In 7	16	43	Analog In 15/7 Return
17	17	Analog In 16/8	18	42	Analog In 24/8 Return
19	16	Analog In 17/9	20	41	Analog In 25/9 Return
21	15	Analog In 18/10	22	40	Analog In 26/10 Return
23	14	Analog In 19/11	24	39	Analog In 27/11 Return
25	13	Analog In 20/12	26	38	Analog In 28/12 Return
27	12	Analog In 21/13	28	37	Analog In 29/13 Return
29	11	Analog In 22/14	30	36	Analog In 30/14 Return
31	10	Analog In 23/15	32	35	Analog In 31/15 Return
33	9	Amp Low	34	34	Analog Ground
35	8	Analog Shield Ground	36	33	Analog Shield Ground
37	7	+15V Output	38	32	Power Ground
39	6	-15V Output	40	31	Reserved
41	5	Analog Output 0+	42	30	Analog Output 0 Return
43	4	Analog Output 1+	44	29	Analog Output 1 Return
45	3	Reserved	46	28	Reserved
47	2	Reserved	48	27	Reserved
49	1	+5V Reference Out	50	26	Analog Ground
51	-	Analog Shield Ground	52	-	Analog Shield Ground
53	-	Analog Shield Ground	54	-	Analog Shield Ground
55	-	Analog Shield Ground	56	-	Analog Shield Ground

## Screw terminal assignments for J2

TB #	J2 Pin #	Description	TB #	J2 Pin #	Description
57	51, 52	Digital Ground	58	17	User Clock Input 0
59	16	User Counter Output 0	60	50	External Gate 0
61	49	Digital Ground	62	15	User Clock Input 1
63	14	User Counter Output 1	64	48	External Gate 1
65	47	Digital Ground	66	13	User Clock Input 2
67	12	User Counter Output 2	68	46	External Gate 2
69	45	Digital Ground	70	11	User Clock Input 3
71	10	User Counter Output 3	72	44	External Gate 3
73	43	Digital Ground	74	9	External D/A Sample Clock In
75	8	External D/A TTL Trigger	76	7	External A/D Sample Clock In
77	6	External A/D TTL Trigger	78	5	A/D Trigger Out
79	4	A/D Sample Clock Out	80	3	Reserved
81	23, 28, 42	Digital Ground	82	39, 41, 57, 62	Digital Ground
83	18, 38, 40, 63, 64	Digital Ground	84	65	Reserved
85	31	Reserved	86	37	Reserved
87	30	Dynamic Digital Output 0	88	29	Dynamic Digital Output 1
89	27	Digital I/O Bank A 0	90	26	Digital I/O Bank A 1
91	25	Digital I/O Bank A 2	92	24	Digital I/O Bank A 3
93	61	Digital I/O Bank A 4	94	60	Digital I/O Bank A 5
95	59	Digital I/O Bank A 6	96	58	Digital I/O Bank A 7
97	22	Digital I/O Bank B 0	98	21	Digital I/O Bank B 1
99	20	Digital I/O Bank B 2	100	19	Digital I/O Bank B 3
101	56	Digital I/O Bank B 4	102	55	Digital I/O Bank B 5
103	54	Digital I/O Bank B 6	104	53	Digital I/O Bank B 7
105	33	Digital Shield Ground	106	68	Analog Ground
107	34	Analog Trigger	108	67	Digital Shield Ground
109	32	Reserved	110	66	Reserved
111	35, 36	Digital Ground	112	1, 2	+5V Out

Refer to the KPCI-3110 and KPCI-3116 User's Manual for more information on attaching the STP-3110 screw terminal accessory.

# Dimensions

Figure 1  
Mounting dimensions (not to scale)

